



CHB150 Series Application Note V12 October 2020

ISOLATED DC-DC CONVERTER CHB150 SERIES APPLICATION NOTE



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4. Technical Specifications

(All specifications are typical at nominal input, full load at 25°C unless otherwise noted.)

ABSOLUTE MAXIMUM RATINGS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Input Voltage						
Continuous		All	-0.3		75	V _{dc}
Transient	100ms	All			100	V _{dc}
Operating Temperature	Case	All	-40		100	°C
Storage Temperature		All	-40		105	°C
Isolation Voltage	1 minute; input/output, input/case, output/case	All	1500			V _{dc}

INPUT CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Operating Input Voltage		All	36	48	75	V _{dc}
Input Under Voltage Lockout						
Turn-On Voltage Threshold		All		34		V _{dc}
Turn-Off Voltage Threshold		All		32.5		V _{dc}
Lockout Hysteresis Voltage		All		1.5		V _{dc}
Maximum Input Current	100% Load, V _{in} =36V	All		4.9		A
No-Load Input Current		All		25		mA
Inrush Current (I ² t)		All			0.1	A ² s
Input Reflected Ripple Current	P-P thru 12uH inductor, 5Hz to 20MHz	All		14		mA

OUTPUT CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Output Voltage Set Point	V _{in} =Nominal V _{in} , I _o = I _{o_max} , T _c =25°C	48S33	3.267	3.3	3.333	V _{dc}
		48S05	4.95	5	5.05	
		48S12	11.88	12	12.12	
		48S15	14.85	15	15.15	
		48S24	23.76	24	24.24	
Output Voltage Regulation						
Load Regulation	I _o =I _{o_min} to I _{o_max}	All			±0.2	%
Line Regulation	V _{in} =low line to high line	All			±0.2	%
Temperature Coefficient	T _c =-40°C to 100°C	All			±0.03	%/°C
Output Voltage Ripple and Noise						
Peak-to-Peak	5Hz to 20MHz bandwidth, Full load 10uF tantalum and 1.0uF ceramic capacitors	V _o = 3.3&5.0V V _o =12&15V V _o =24V			100 150 240	mV
RMS	5Hz to 20MHz bandwidth, Full load, 10uF solid tantalum and 1.0uF ceramic capacitors	V _o = 3.3&5.0V V _o =12&15V V _o =24V			40 60 100	mV



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PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Operating Output Current Range		48S33	0		30	A
		48S05	0		30	
		48S12	0		12.5	
		48S15	0		10	
		48S24	0		6.25	
Output DC Current Limit Inception	Output Voltage=90% Nominal Output Voltage	All	110		140	%
Maximum Output Capacitance	Full load (resistive)	48S33	0		30000	uF
		48S05	0		30000	
		48S12	0		12500	
		48S15	0		10000	
		48S24	0		6250	

DYNAMIC CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Output Voltage Current Transient						
Step Change in Output Current	75% to 100% of I_{o_max}	All			±5	%
Setting Time (within 1% V_{out} nominal)	$dI/dt=0.1A/us$	All			500	us
Turn-On Delay and Rise Time						
Turn-On Delay Time, From On/Off Control	$V_{on/off}$ to 10% V_{o_set}	All		8		ms
Turn-On Delay Time, From Input	V_{in_min} to 10% V_{o_set}	All		5		ms
Output Voltage Rise Time	10% V_{o_set} to 90% V_{o_set}	All		2		ms

EFFICIENCY

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
100% Load	V_{in} =Nominal V_{in}	48S33		82		%
		48S05		86		
		48S12		89		
		48S15		89		
		48S24		89		

ISOLATION CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Isolation Voltage	1 minute; input/output, input/case, output/case	All			1500	V_{dc}
Isolation Resistance		All	10			MΩ
Isolation Capacitance		All		1000		pF

FEATURE CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Switching Frequency		All		500		KHz
On/Off Control, Positive Remote On/Off logic						
Logic Low (Module Off)	$V_{on/off}$ at $I_{on/off}=1.0mA$	All	0		0.8	V
Logic High (Module On)	$V_{on/off}$ at $I_{on/off}=0.0uA$	All	Open Circuit		75	V



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PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
On/Off Control, Negative Remote On/Off logic						
Logic High (Module Off)	$V_{on/off}$ at $I_{on/off}=0.0\mu A$	All	Open Circuit		75	V
Logic Low (Module On)	$V_{on/off}$ at $I_{on/off}=1.0mA$	All	0		0.8	V
Off Converter Input Current	Shutdown input idle current	All			10	mA
Output Voltage Trim Range	$P_{out}=\text{max rated power}$	All	-10		+10	%
Output Over Voltage Protection		All	115	125	140	%
Over-Temperature Protection	Shutdown Case Temperature	All		100		°C
	Restart threshold Case Temperature	All		70		°C

GENERAL SPECIFICATIONS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
MTBF	$I_o=100\%$ of I_{o_max} ; $T_a=25^\circ C$ per MIL-HDBK-217F	All		900		K hours
Weight		All		100		grams



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5. Main Features and Functions

5.1 Operating Temperature Range

The CHB150 series converters can be operated within a wide case temperature range of -40°C to 100°C . Consideration must be given to the derating curves when ascertaining maximum power that can be drawn from the converter. The maximum power drawn from half brick models is influenced by usual factors, such as:

- Input voltage range
- Output load current
- Forced air or natural convection

5.2 Output Voltage Adjustment

Section 6.8 describes in detail how to trim the output voltage with respect to its set point. The output voltage on all models is adjustable within the range of +10% to -10%.

5.3 Over Current Protection

All models have internal over current and continuous short circuit protection. The unit operates normally once the fault condition is removed. At the point of current limit inception, the output voltage of converter will be going down into current limit and power fold-back protection.

5.4 Output Over Voltage Protection

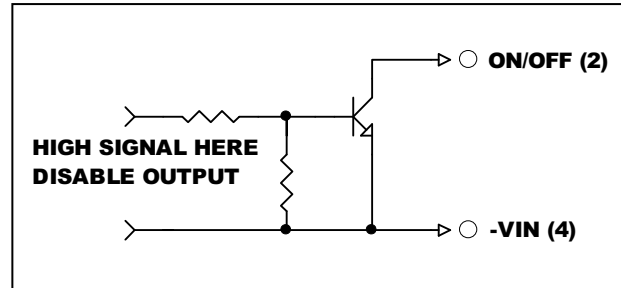
The output over voltage protection consists of circuitry that internally limits the output voltage. If more accurate output over voltage protection is required then an external circuit can be used via the remote on/off pin.

5.6 Remote On/Off

The CHB150 series allows the user to switch the module on and off electronically with the remote on/off feature. All models are available in "positive logic" and "negative logic" (optional) versions. The converter turns on if the remote on/off pin is high (open circuit). Setting the pin low (0 to $<0.8\text{Vdc}$) will turn the converter off. The signal level of the remote on/off input is defined with respect to ground. If not using the remote on/off pin, leave the pin open (converter will be on). Models with part number suffix "N" are the "negative logic" remote on/off version. The unit turns off if the remote on/off pin is high (open circuit). The converter turns on if the on/off pin input is low (0 to $<0.8\text{Vdc}$). Note that the converter is off by default.

5.7 UVLO (Under Voltage Lock Out)

Input under voltage lockout is standard on the CHB150 unit. The unit will shut down when the input voltage drops below a threshold, and the unit will operate when the input voltage goes above the upper threshold.



5.8 Over Temperature Protection

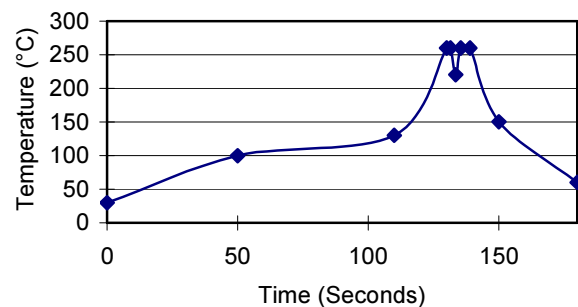
These modules have an over temperature protection circuit to safeguard against thermal damage. The module shuts down and latches off when the maximum case reference temperature is exceeded. The module will restart when the case temperature falls below restart threshold.

6. Applications

6.1 Recommended Layout, PCB Footprint and Soldering Information

The system designer or end user must ensure that metal and other components in the vicinity of the converter meet the spacing requirements for which the system is approved. Low resistance and inductance PCB layout traces are the norm and should be used where possible. Due consideration must also be given to proper low impedance tracks between power module, input and output grounds. The recommended soldering profile and PCB layout are shown below.

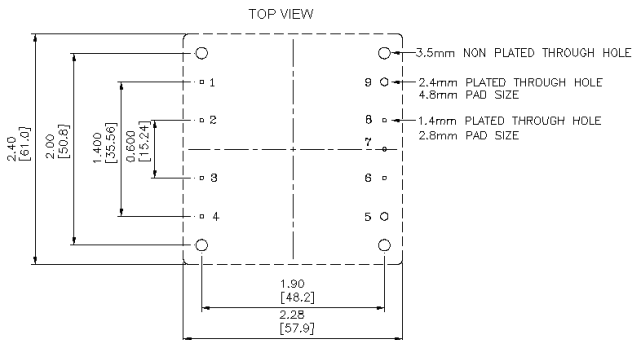
Lead Free Wave Soldering Profile





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6.2 Convection Requirements for Cooling

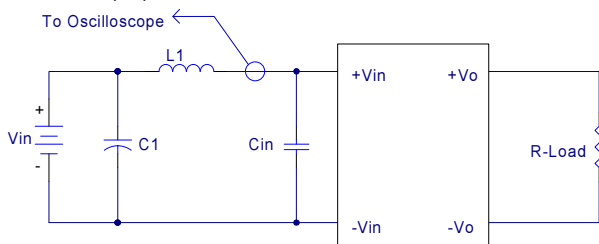
To predict the approximate cooling needed for the half brick module, refer to the power derating curves in section 6.4. These derating curves are approximations of the ambient temperatures and airflows required to keep the power module temperature below its maximum rating. Once the module is assembled in the actual system, the module's temperature should be monitored to ensure it does not exceed 100°C as measured at the center of the top of the case (thus verifying proper cooling).

6.3 Thermal Considerations

The power module operates in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat is removed by conduction, convection, and radiation to the surrounding environment. The example is presented in section 6.4. The power output of the module should not be allowed to exceed rated power ($V_{o_set} \times I_{o_max}$).

6.4 Input Capacitance at The Power Module

The converters must be connected to low AC source impedance. To avoid problems with loop stability source inductance should be low. Also, the input capacitors (C_{in}) should be placed close to the converter input pins to decouple distribution inductance. However, the external input capacitors are chosen for suitable ripple handling capability. Low ESR capacitors are good choice. Circuit as shown as below represents typical measurement methods for reflected ripple current. C_1 and L_1 simulate a typical DC source impedance. The input reflected-ripple current is measured by current probe to oscilloscope with a simulated source Inductance (L_1).



L1: Short
 C1: NC
 Cin: NC
 Input Reflected-Ripple Test Setup

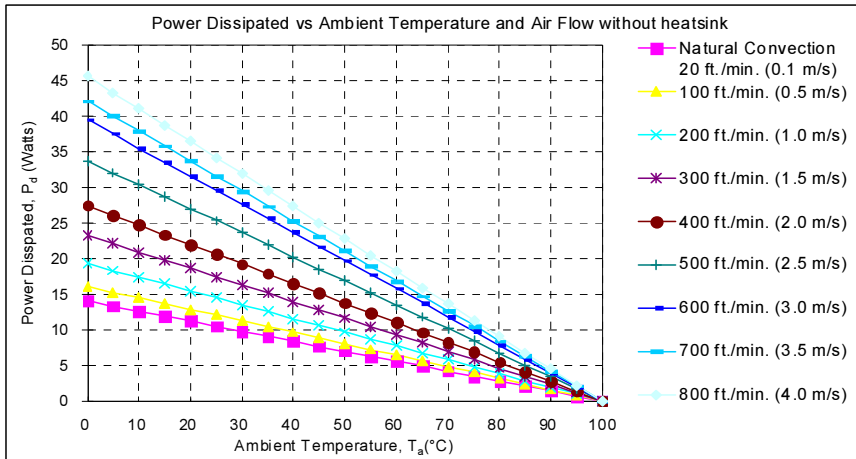


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6.5 Power Derating

The operating case temperature range of CHB150 series is -40°C to $+100^{\circ}\text{C}$. When operating the CHB150 series, proper derating or cooling is needed. The maximum case temperature under any operating condition should not exceed 100°C .



AIR FLOW RATE	TYPICAL R_{ca}
Natural Convection	7.12°C/W
20ft./min. (0.1m/s)	6.21°C/W
100 ft./min. (0.5m/s)	6.21°C/W
200 ft./min. (1.0m/s)	5.17°C/W
300 ft./min. (1.5m/s)	4.29°C/W
400 ft./min. (2.0m/s)	3.64°C/W
500 ft./min. (2.5m/s)	2.96°C/W
600 ft./min. (3.0m/s)	2.53°C/W
700 ft./min. (3.5m/s)	2.37°C/W
800 ft./min. (4.0m/s)	2.19°C/W

Example:

What is the minimum airflow necessary for a CHB150-48S12 operating at nominal line voltage, an output current of 12.5A, and a maximum ambient temperature of 40°C

Solution:

Given:

$$V_{in}=48\text{Vdc}, V_o=12\text{Vdc}, I_o=12.5\text{A}$$

Determine Power dissipation (P_d):

$$P_d = P_i - P_o = P_o(1-\eta)/\eta$$

$$P_d = 12 \times 12.5 \times (1-0.89)/0.89 = 18.54\text{Watts}$$

Determine airflow:

$$\text{Given: } P_d = 18.54\text{W and } T_a = 40^{\circ}\text{C}$$

Check above Power Derating curve:

Minimum airflow = 500 ft./min.

Verify:

The maximum temperature rise

$$\Delta T = P_d \times R_{ca} = 18.54 \times 2.96 = 54.88^{\circ}\text{C}$$

The maximum case temperature

$$T_c = T_a + \Delta T = 94.88^{\circ}\text{C} < 100^{\circ}\text{C}$$

Where:

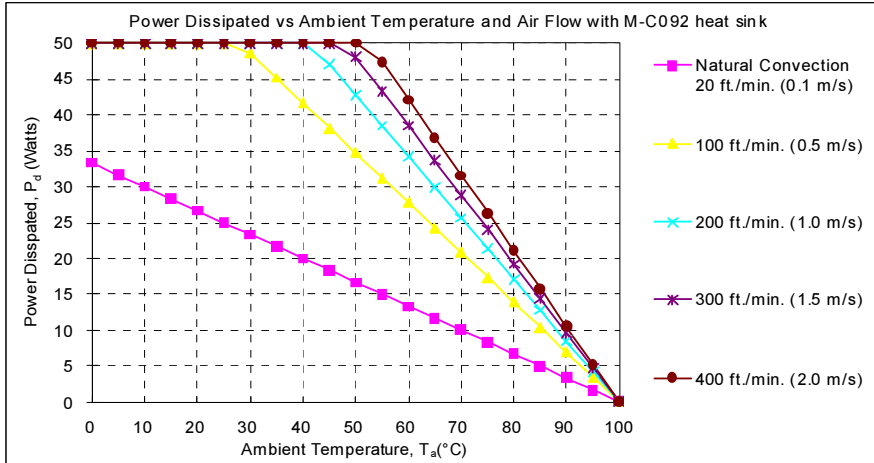
The R_{ca} is thermal resistance from case to ambience.

The T_a is ambient temperature and the T_c is case temperature.



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AIR FLOW RATE	TYPICAL R_{ca}
Natural Convection 20ft./min. (0.1m/s)	3°C/W
100 ft./min. (0.5m/s)	1.44°C/W
200 ft./min. (1.0m/s)	1.17°C/W
300 ft./min. (1.5m/s)	1.04°C/W
400 ft./min. (2.0m/s)	0.95°C/W

Example with heatsink HBT254 (M-C092):

What is the minimum airflow necessary for a CHB150-48S12 operating at nominal line voltage, an output current of 12.5A, and a maximum ambient temperature of 40°C

Solution:

Given:

$$V_{in}=48V_{dc}, V_o=12V_{dc}, I_o=12.5A$$

Determine Power dissipation (Pd):

$$P_d=P_i-P_o=P_o(1-\eta)/\eta$$

$$P_d=12 \times 12.5 \times (1-0.89)/0.89=18.54 \text{ Watts}$$

Determine airflow:

$$\text{Given: } P_d=18.54W \text{ and } T_a=40^\circ C$$

Check above Power de-rating curve:

$$P_d < 20W, \text{ Natural Convection}$$

Verify: Maximum temperature rise is $\Delta T = P_d \times R_{ca}=18.54 \times 3=55.62^\circ C$

$$\text{Maximum case temperature is } T_c=T_a+\Delta T=95.62^\circ C < 100^\circ C$$

Where: The R_{ca} is thermal resistance from case to ambient environment.

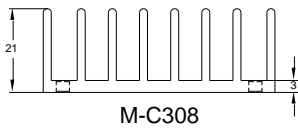
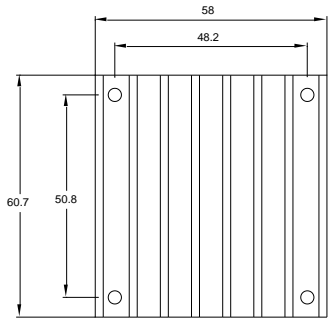
T_a is ambient temperature and T_c is case temperature.



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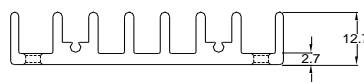
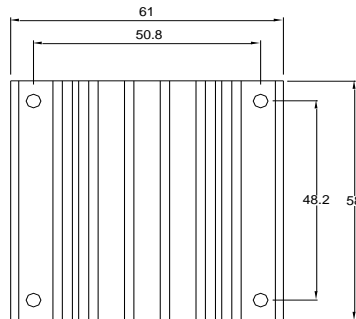
6.6 Half Brick Heat Sinks:



M-C308

HBL210 (M-C308) G6620400201
Longitudinal Heat Sink

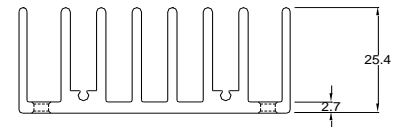
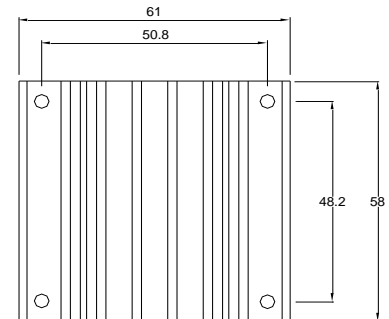
Rca:
 3.90°C/W (typ.), natural convection
 1.74°C/W (typ.), at 100LFM
 1.33°C/W (typ.), at 200LFM
 1.12°C/W (typ.), at 300LFM
 0.97°C/W (typ.), at 400LFM



M-C091

HBT127 (M-C091) G6610120402
Transverse Heat Sink

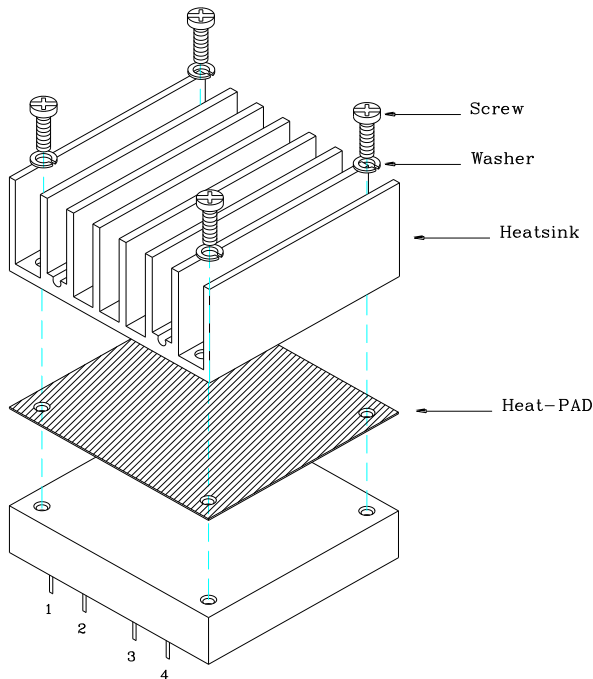
Rca:
 4.70°C/W (typ.), natural convection
 2.89°C/W (typ.), at 100LFM
 2.30°C/W (typ.), at 200LFM
 1.88°C/W (typ.), at 300LFM
 1.59°C/W (typ.), at 400LFM



M-C092

HBT254 (M-C092) G6610130402
Transverse Heat Sink

Rca:
 3.00°C/W (typ.), natural convection
 1.44°C/W (typ.), at 100LFM
 1.17°C/W (typ.), at 200LFM
 1.04°C/W (typ.), at 300LFM
 0.95°C/W (typ.), at 400LFM

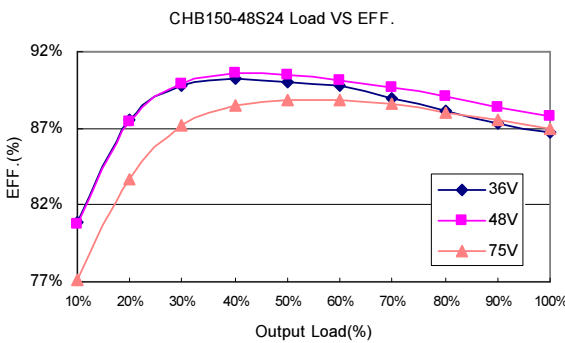
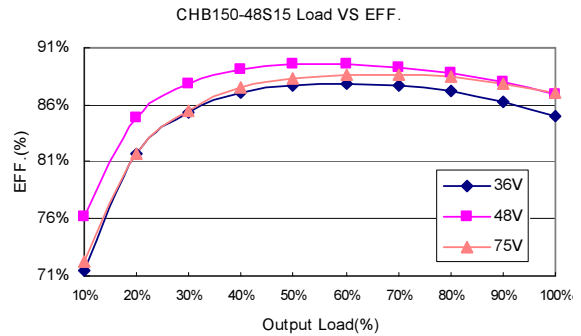
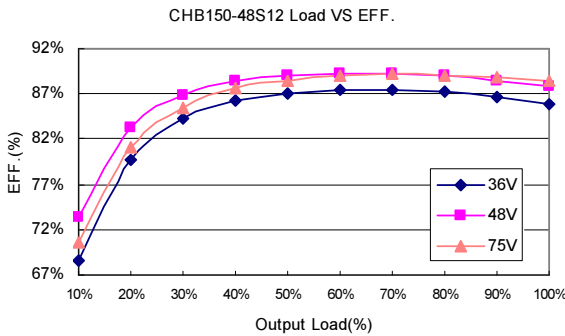
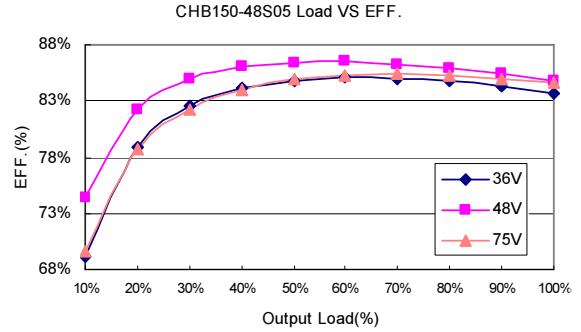
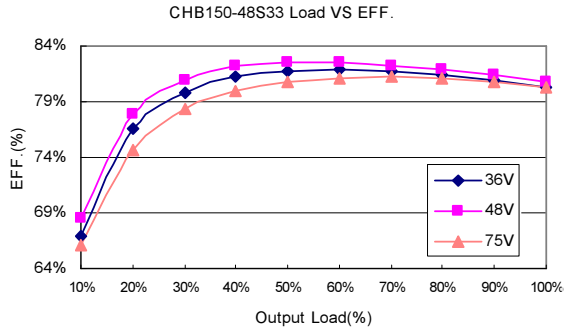


THERMAL PAD PH01: SZ 56.9*60*0.25 mm (G6135041091)
 SCREW K308W: SMP+SW M3*8L (G75A1300322)



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6.7 Efficiency VS. Load





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6.8 Test Set-Up

The basic test set-up to measure parameters such as efficiency and load regulation is shown below. When testing the modules under any transient conditions please ensure that the transient response of the source is sufficient to power the equipment under test. We can calculate:

- Efficiency
- Load regulation and line regulation.

The value of efficiency is defined as:

$$\eta = \frac{V_o \times I_o}{V_{in} \times I_{in}} \times 100\%$$

Where:

V_o is output voltage.

I_o is output current.

V_{in} is input voltage.

I_{in} is input current.

The value of load regulation is defined as:

$$Load.reg = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

Where:

V_{FL} is the output voltage at full load

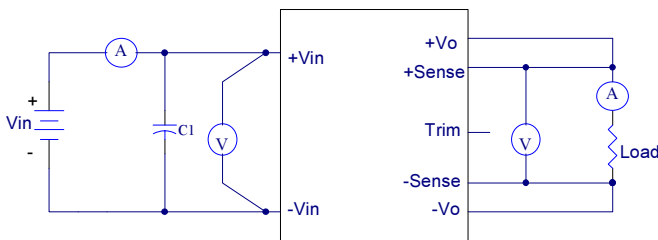
V_{NL} is the output voltage at no load

The value of line regulation is defined as:

$$Line.reg = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Where:

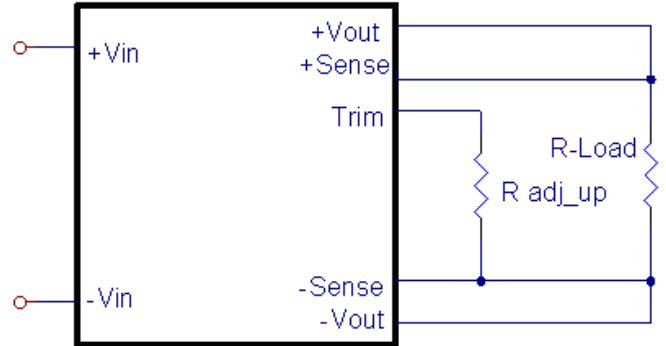
V_{HL} is the output voltage of maximum input voltage at full load. V_{LL} is the output voltage of minimum input voltage at full load.



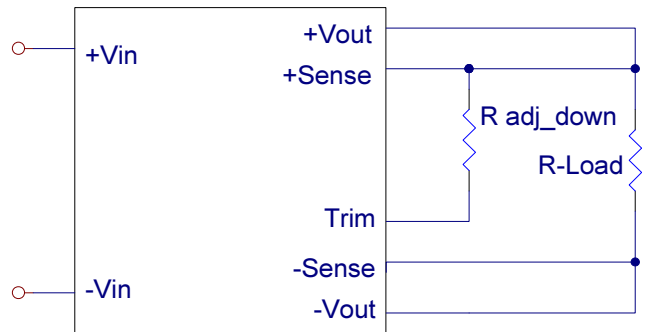
CHB150W Series Test Setup

6.9 Output Voltage Adjustment

In order to trim the voltage up or down, one needs to connect the trim resistor either between the trim pin and $-V_o$ for trim-up or between trim pin and $+V_o$ for trim-down. The output voltage trim range is $\pm 10\%$. This is shown:



Trim-up Voltage Setup



Trim-down Voltage Setup

V_{out} (V)	R1 (K Ω)	R2 (K Ω)	R3 (K Ω)	V_r (V)	V_f (V)
3.3V	3	12	4.3	1.24	0.46
5V	2.32	3.3	NC	2.5	0
12V	9.1	51	5.1	2.5	0.46
15V	12	56	8.25	2.5	0.46
24V	20	100	7.5	2.5	0.46

Table of trim resistor values



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The value of R_{adj_up} defined as:

For 5V output module

$$R_{adj_up} = \left(\frac{R_1 V_r}{V_o - V_{o_nom}} \right) - R_2 \quad (\text{K}\Omega)$$

For other output module

$$R_{adj_up} = \left(\frac{R_1 (V_r - V_f \left(\frac{R_2}{R_2 + R_3} \right))}{V_o - V_{o_nom}} \right) - \frac{R_2 R_3}{R_2 + R_3} \quad (\text{K}\Omega)$$

Where:

- R_{adj_up} is the external resistor in $\text{K}\Omega$.
- V_{o_nom} is the nominal output voltage.
- V_o is the desired output voltage.
- R_1, R_2, R_3 and V_r are internal components and are defined in the table of trim resistor values

For example, to trim-up the output voltage of 5V module (CHB150-48S05) by 8% to 5.4V, R_{adj_up} is calculated as follows:

$$V_o - V_{o_nom} = 5.4 - 5.0 = 0.4\text{V}$$

$$R_1 = 2.32 \text{ K}\Omega, R_2 = 3.3 \text{ K}\Omega,$$

$$V_r = 2.5 \text{ V}$$

$$R_{adj_up} = \frac{5.8}{0.4} - 3.3 = 11.2 \quad (\text{K}\Omega)$$

The value of R_{adj_down} defined as:

$$R_{adj_down} = \frac{R_1 \times (V_o - V_r)}{V_{o_nom} - V_o} - R_2 \quad (\text{K}\Omega)$$

Where:

- R_{adj_down} is the external resistor in $\text{K}\Omega$.
- V_{o_nom} is the nominal output voltage.
- V_o is the desired output voltage.
- R_1, R_2, R_3 and V_r are internal components.

For example: to trim-down the output voltage of 12V module (CHB150-48S05) by 8% to 4.6V, R_{adj_down} is calculated as follows:

$$V_{o_nom} - V_o = 5.0 - 4.6 = 0.4 \text{ V}$$

$$R_1 = 2.32 \text{ K}\Omega, R_2 = 3.3 \text{ K}\Omega, V_r = 2.5 \text{ V}$$

$$R_{adj_down} = \frac{2.32 \times (4.6 - 2.5)}{0.4} - 3.3 = 8.88 \quad (\text{K}\Omega)$$

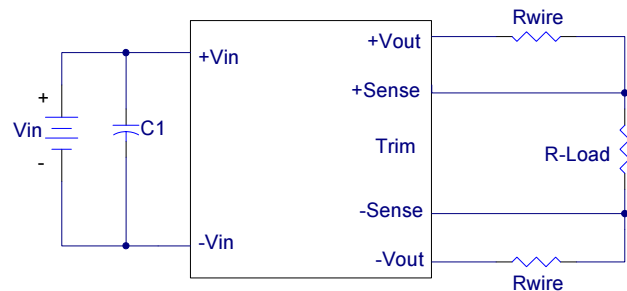
6.10 Output Remote Sensing

The CHB150 Series converter has the capability to remotely sense both lines of its output. This feature moves the effective output voltage regulation point from the output of the unit to the point of connection of the remote sense pins. This feature automatically adjusts the real output voltage of the CHB150 series in order to compensate for voltage drops in distribution and maintain a regulated voltage at the point of load. The remote-sense voltage range is:

$$[(+V_{out}) - (-V_{out})] - [(+Sense) - (-Sense)] \leq 10\% \text{ of } V_{o_nominal}$$

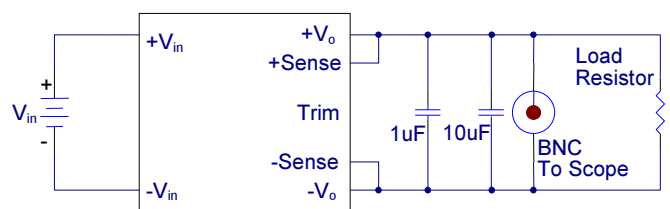
If the remote sense feature is not to be used, the sense pins should be connected locally. The +Sense pin should be connected to the +Vout pin at the module and the -Sense pin should be connected to the -Vout pin at the module.

This is shown in the schematic below.



Note: Although the output voltage can be varied (increased or decreased) by both remote sense and trim, the maximum variation for the output voltage is the larger of the two values not the sum of the values. The output power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. Using remote sense and trim can cause the output voltage to increase and consequently increase the power output of the module if output current remains unchanged. Always ensure that the output power of the module remains at or below the maximum rated power. Also be aware that if V_{o_set} is below nominal value, P_{out_max} will also decrease accordingly because I_{o_max} is an absolute limit. Thus, $P_{out_max} = V_{o_set} \times I_{o_max}$ is also an absolute limit.

6.11 Output Ripple and Noise



Output ripple and noise is measured with 1.0uF ceramic and 10uF solid tantalum capacitors across the output.



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6.12 Output Capacitance

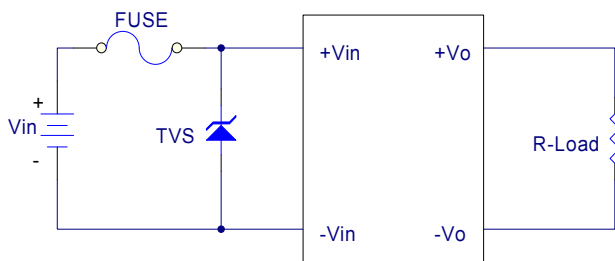
The CHB150 series converters provide unconditional stability with or without external capacitors. For good transient response, low ESR output capacitors should be located close to the point of load. PCB design emphasizes low resistance and inductance tracks in

consideration of high current applications. Output capacitors with their associated ESR values have an impact on loop stability and bandwidth. Cincon's converters are designed to work with load capacitance to see technical specifications.

7. Safety & EMC

7.1 Input Fusing and Safety Considerations

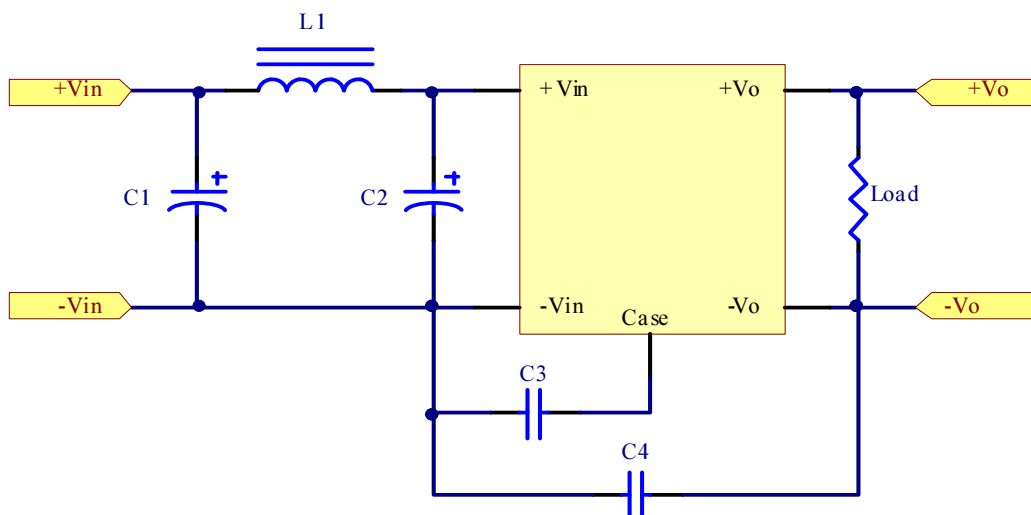
The CHB150 series converters have no internal fuse. In order to achieve maximum safety and system protection, always use an input line fuse. We recommended a 6.3A fast acting fuse. It is recommended that the circuit have a transient voltage suppressor diode (TVS) across the input terminal to protect the unit against surge or spike voltage and input reverse voltage (as shown).



7.2 EMC Considerations

Suggested Circuits for Conducted EMI Class A & Class B

(1) EMI and conducted noise meet EN55032 Class A specifications:



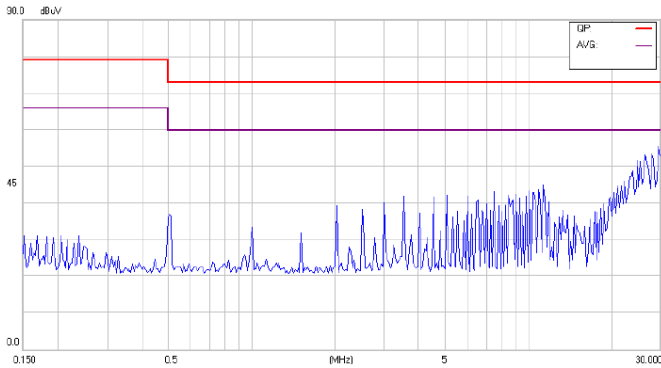
Model No.	C1	C2	C3	C4	L1
CHB150-48S33	47uF/100V	47uF/100V	1000pF	1000pF	3.4uH
CHB150-48S05	47uF/100V	47uF/100V	1000pF	1000pF	3.4uH
CHB150-48S12	47uF/100V	47uF/100V	1000pF	1000pF	3.4uH
CHB150-48S15	47uF/100V	47uF/100V	1000pF	1000pF	3.4uH
CHB150-48S24	47uF/100V	47uF/100V	1000pF	1000pF	3.4uH

Note: 47uF/100V aluminum capacitors, C3, C4 is ceramic capacitors.

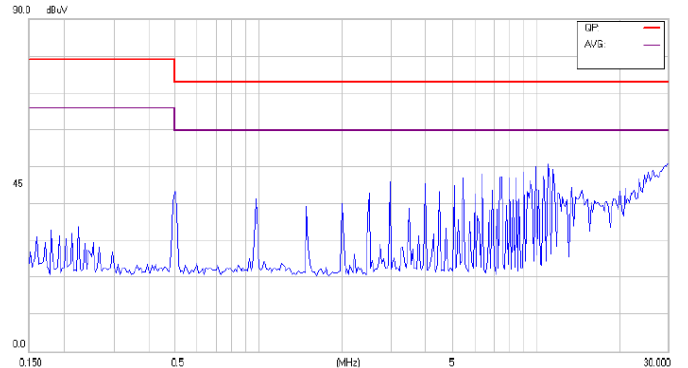


CHB150 Series

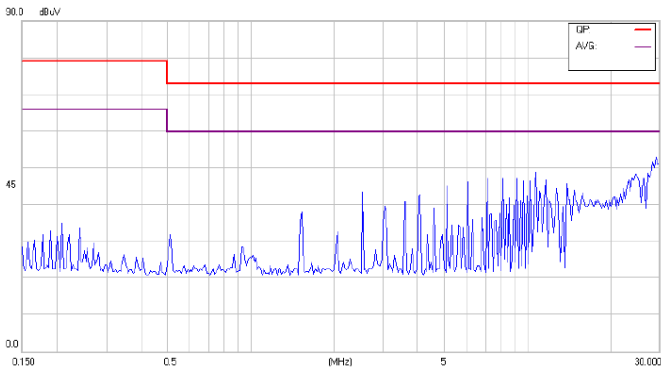
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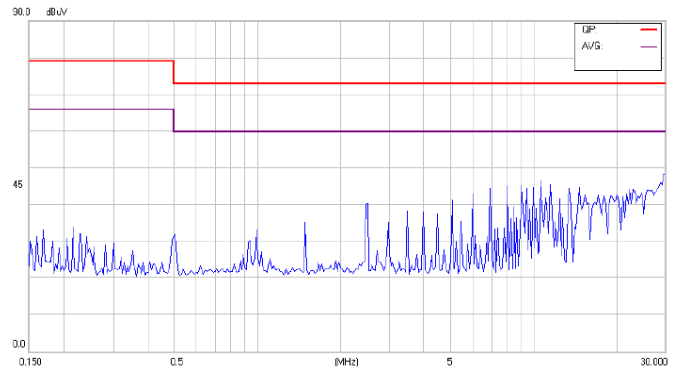
Conducted Class A of CHB150-48S33



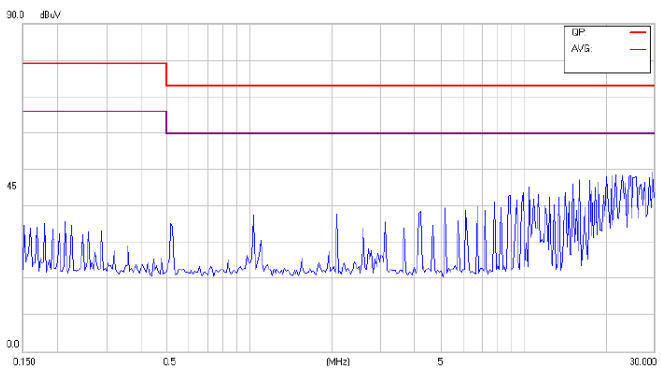
Conducted Class A of CHB150-48S05



Conducted Class A of CHB150-48S12



Conducted Class A of CHB150-48S15

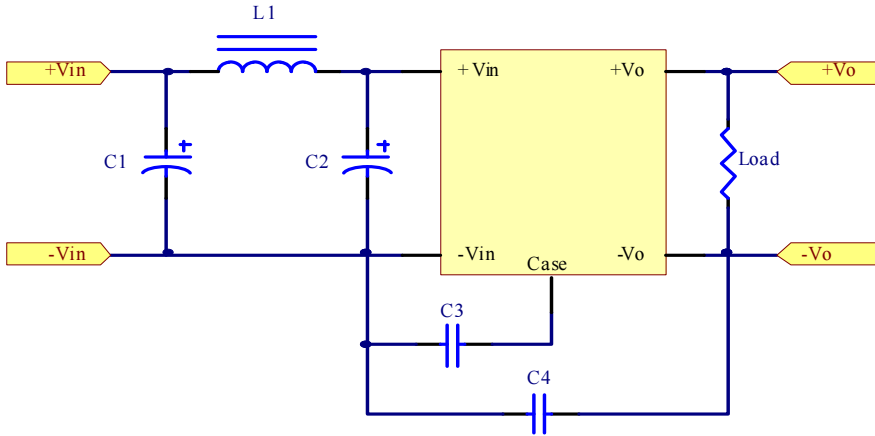


Conducted Class A of CHB150-48S24



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(2) EMI and conducted noise meet EN55032 Class B specifications:

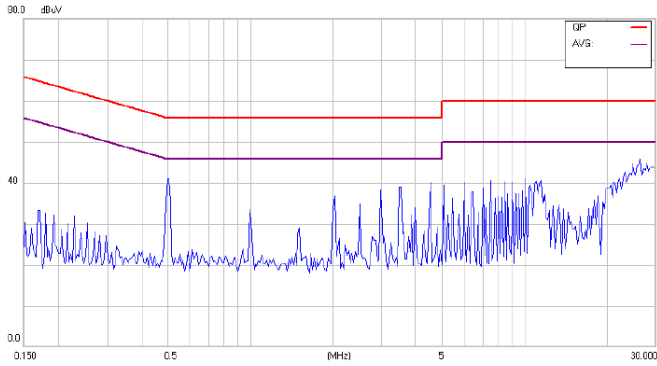


Model No.	C1	C2	C3	C4	L1
CHB150-48S33	82uF/100V	82uF/100V	1500pF	1500pF	3.4uH
CHB150-48S05	82uF/100V	82uF/100V	1500pF	1500pF	3.4uH
CHB150-48S12	82uF/100V	82uF/100V	1500pF	1500pF	3.4uH
CHB150-48S15	82uF/100V	82uF/100V	1500pF	1500pF	3.4uH
CHB150-48S24	82uF/100V	82uF/100V	1500pF	1500pF	3.4uH

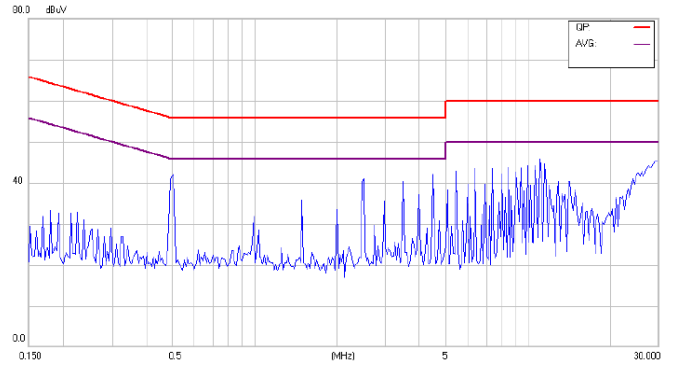
Note: 82uF/100V aluminum capacitors, C3, C4 is ceramic capacitors.



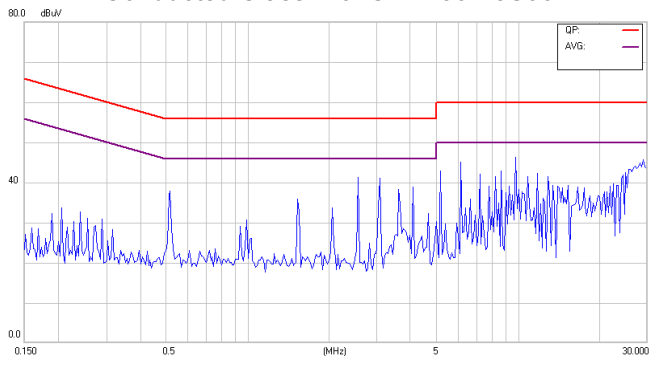
CHB150 Series Application Note V12 October 2020



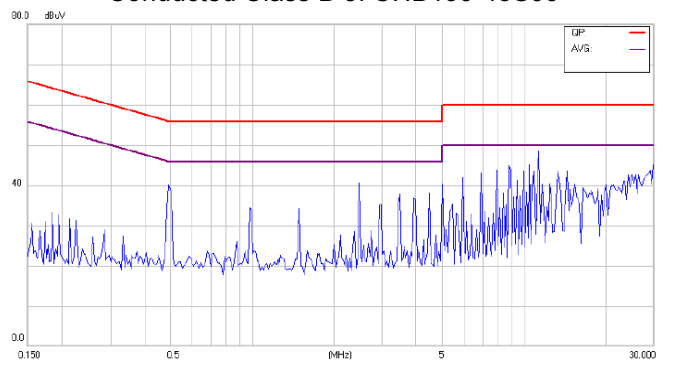
Conducted Class B of CHB150-48S33



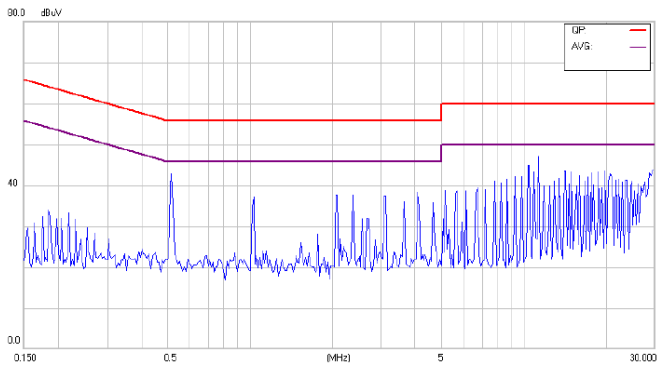
Conducted Class B of CHB150-48S05



Conducted Class B of CHB150-48S12



Conducted Class B of CHB150-48S15



Conducted Class B of CHB150-48S24



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8. Part Number

Format: CHB150 – II X OO L Y

Parameter	Series	Nominal Input Voltage	Number of Outputs	Output Voltage	Remote ON/OFF Logic	Mounting Inserts
Symbol	CHB150	II	X	OO	L	Y (Option)
Value	CHB150	48: 48 Volts	S: Single	33: 3.3 Volts 05: 05 Volts 12: 12 Volts 15: 15 Volts 24: 24 Volts	None: Positive N: Negative	-C: Clear Mounting Insert (3.2mm DIA.)

9. Mechanical Specifications

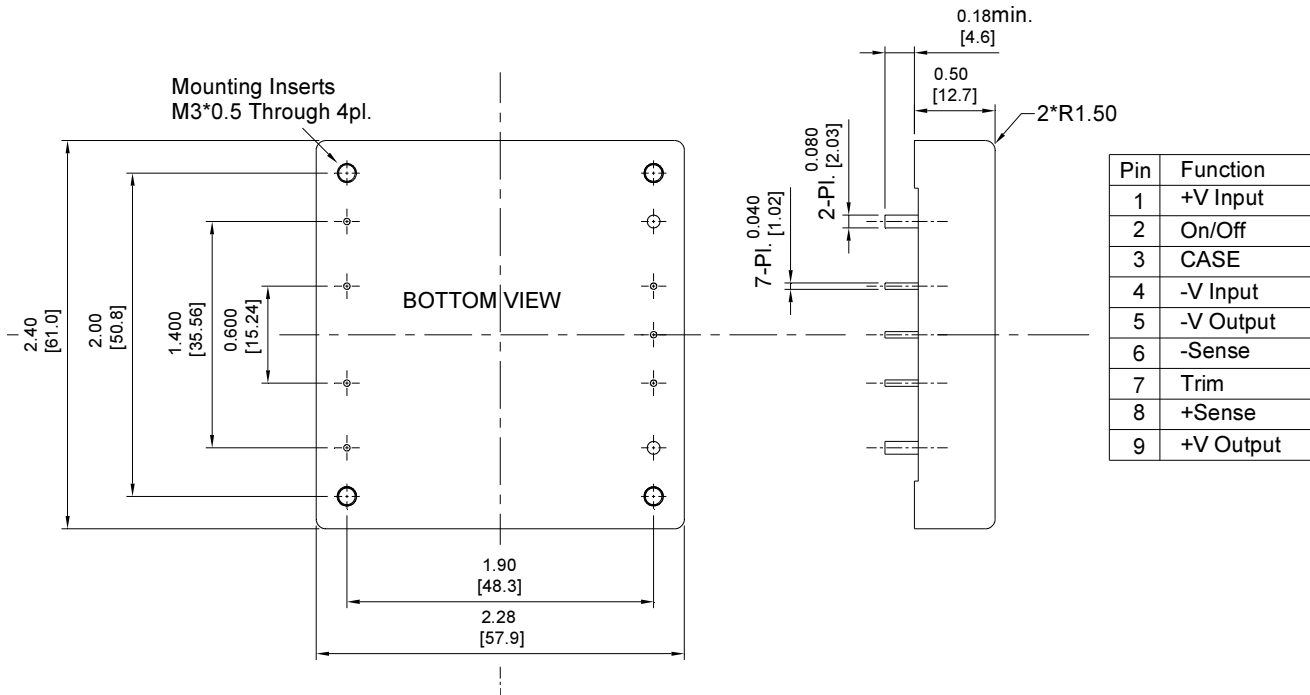
9.1 Mechanical Outline Diagrams

CASE HB

All Dimensions In Inches(mm)

Tolerances Inches: X.XX= ±0.02 , X.XXX= ±0.010

Millimeters: X.X= ±0.5 , X.XX=±0.25



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