

ISOLATED DC-DC Converter EC3A-E SERIES APPLICATION NOTE



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Application Note V10 June 2014

Content

1. INTRODUCTION	3
2. DC-DC CONVERTER FEATURES	3
3. ELECTRICAL BLOCK DIAGRAM	3
4. TECHNICAL SPECIFICATIONS	4
5. MAIN FEATURES AND FUNCTIONS	8
5.1 Operating Temperature Range	8
5.2 UVLO (Under Voltage Lock Out)	8
5.3 Over Current Protection	8
6. APPLICATIONS	8
6.1 Recommended Layout PCB Footprints and Soldering Information	8
6.2 Power De-Rating Curves for EC3A-E Series	9
6.3 Efficiency vs. Load Curves	10
6.4 Input Capacitance at the Power Module	15
6.5 Test Set-Up	15
6.6 Output Ripple and Noise Measurement	15
6.7 Output Capacitance	16
7. SAFETY & EMC	16
7.1 Input Fusing and Safety Considerations.	16
7.2 EMC Considerations	16
8. PART NUMBER	19
9. MECHANICAL SPECIFICATIONS	20



Application Note V10 June 2014

1. Introduction

The EC3A-E series offer 2-3 watts of output power in a 24 pin DIP and SMD package. The EC3A-E series has a 2:1 wide input voltage range of 4.5-9VDC, 9-18VDC, 18-36VDC and 36-72VDC, and provides a precisely regulated output. This series has features such as high efficiency, 500VDC, 1.5KVDC, 3KVDC of isolation and allows an ambient operating temperature range of ambient operating temperature range of −40℃ to 85° C . The modules are fully protected against output short circuit. All models are very suitable for distributed power architectures, telecommunications, battery operated equipment and industrial applications.

2. DC-DC Converter Features

- * 2-3W Isolated Output
- * DIP-24 / SMD Package
- * Efficiency Up to 87%
- * 2:1 Input Range
- * Regulated Outputs
- * PI Input Filter
- * Continuous Short Circuit Protection
- * No Tantalum Capacitor Inside
- * Input UVLO (Under Voltage Lockout)
- * Meet EMI EN55022 class A
- * Wide Operating Temperature Range
- * UL60950-1 Approval

3. Electrical Block Diagram

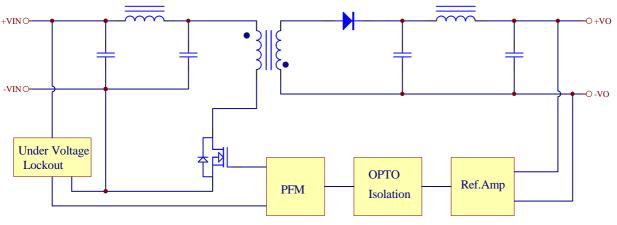


Figure 1 Electrical Block Diagram of single output module

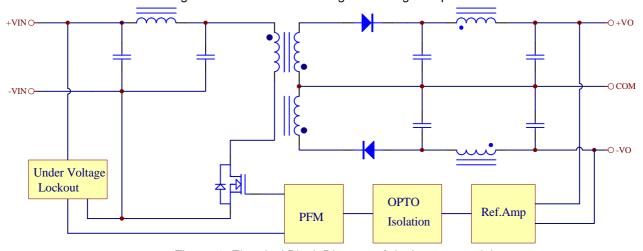


Figure 2 Electrical Block Diagram of dual output module



4. Technical Specifications(All specifications are typical at nominal input, full load at 25°C unless otherwise noted.)

ABSOLUTE MAXIMUM RA PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Input Voltage	INOTES AND SONDITIONS	Device	IVIIII.	i ypicai	iviax.	Utilis
mpat voltago		5Vin	-0.3		9	
Continuous		12Vin	-0.3		18	Vdc
		24Vin	-0.3		36	
		48Vin	-0.3		72	
		5Vin			10	
T	100	12Vin			25	V/.1-
Transient	100ms	24Vin			50	Vdc
		48Vin			100	
Operating Ambient Temperature		All	-40		+85	$^{\circ}\!\mathbb{C}$
Case Temperature		All			+100	$^{\circ}\!\mathbb{C}$
Storage Temperature		All	-40		+100	$^{\circ}\!\mathbb{C}$
, , , , , , ,		EC3AXX	500			Ţ,
		(M/MS)-E	500			
Input/Output Isolation Voltage	1 minute	EC3AXX	3000			Vdc
paa caspat isolalion i sitage		(H/HS)-E				
		EC3AXX (HM/HMS)-E	1500			
INPUT CHARACTERISTICS		[(□IVI/□IVIO)-E]				
PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
FANAMETEN	NOTES and CONDITIONS	5Vin	4.5	5	9	Ullita
		12Vin	9	12	18	
Operating Input Voltage		24Vin	18	24	36	Vdc
		48Vin	36	48	72	
		5Vin	4.0	4.2	4.4	
Turn-On Voltage Threshold		12Vin	8	8.5	8.8	Vdc
		24Vin	16	17	17.5	
		48Vin 5Vin	31.5 3.8	33	34 4.2	
		12Vin	3.6 7.7	4.0 8	8.3	
Turn-Off Voltage Threshold		24Vin	15	16	17	Vdc
		48Vin	30.5	31	33	
		5Vin		0.2		
Lockout Hysteresis Voltage		12Vin		0.5		Vdc
Lockout Hysteresis voltage		24Vin		1.0		vuc
		48Vin		2.0		
	Full load, Vin=4.5V	5Vin		880		
Maximum Input Current	Full load, Vin= 9V 12Vin			420		mA
•	Full load, Vin=18V Full load, Vin=36V	24Vin 48Vin		210 100		
	i uii ioau, viii=30 v	Vo=3.3Vdc		15		
		Vo=5Vdc		15		
		Vo=12Vdc		15		
No-Load Input Current	Vin=5V	Vo=15Vdc		15		mA
•		Vo=±5Vdc		25		
		Vo=±12Vdc		25		
		Vo=±15Vdc		25		



Vo=3.3Vdc 7.5 Vo=5Vdc 7.5 Vo=12Vdc 10 Vin=12V Vo=15Vdc 10 Vo=±5Vdc 15 Vo=±12Vdc 12 Vo=±15Vdc 15	
Vo=12Vdc	
Vin=12V	
Vo=±5Vdc 15 Vo=±12Vdc 12	
Vo=±12Vdc 12	
Vo=±12Vdc 12	
Vo=3.3Vdc 5	mA
Vo=5.5vdc	
Vo=±5Vdc 7.5	
Vo=±12Vdc	
Vo=±15Vdc 10	
Vo=3.3Vdc 3	
Vo=5Vdc 3	
Vo=12Vdc 3	
Vin=48V	mΑ
Vo=±5Vdc 5	
Vo=±12Vdc 5	
Vo=±15Vdc 5	
Inrush Current (I ² t) As per ETS300 132-2 All 0.01	A ² s
Input Reflected-Ripple Current P-P thru 12uH inductor, 5Hz to 20MHz All 10	mA
OUTPUT CHARACTERISTIC	
PARAMETER NOTES and CONDITIONS Device Min. Typical Max.	Units
Vo=3.3Vdc 3.2505 3.3 3.3495	00
Vo=5Vdc 4.925 5 5.075	
Vo=12Vdc 11.82 12 12.18	
	Vdc
Output Voltage Set Point $Vin=nominal input, Io=Io_{max.}$ $Vo=15Vdc$ 14.775 15 15.225 15.075	vac
Vo=±12Vdc ±11.82 ±12 ±12.18	
Vo=±15Vdc ±14.775 ±15 ±15.225	
Output Voltage Balance Vin=nominal input, Io=Io _{max.} Dual ±1.0	%
Output Voltage Regulation	
I I I I I I I I I I I I I I I I I I I	%
Load Regulation Io=full load to 10% load Single ±0.5	
lo=full load to 25% load Dual ±1.0	
Load Regulation Io=full load to 25% load Dual ±1.0 Line Regulation Vin=low line to high line full load Single ±0.5	%
Line Regulation Io=full load to 25% load Dual ±1.0	
	% %/°C
Line Regulation Line Regulation Vin=low line to high line, full load Temperature Coefficient Ta=-40°C to 85°C Output Voltage Ripple and Noise (5Hz to 20MHz bandwidth) Line Regulation Vin=low line to high line, full load Dual ±0.5 All ±0.05	
Load Regulation Io=full load to 25% load Dual ±1.0 Line Regulation Vin=low line to high line, full load Single Dual ±0.5 Temperature Coefficient Ta=-40°C to 85°C All ±0.05 Output Voltage Ripple and Noise (5Hz to 20MHz bandwidth) Vo=3.3Vdc Vo=3.3Vdc	
Load Regulation Io=full load to 25% load Dual ±1.0 Line Regulation Vin=low line to high line, full load Single Dual ±0.5 Temperature Coefficient Ta=-40°C to 85°C All ±0.05 Output Voltage Ripple and Noise (5Hz to 20MHz bandwidth)	
Load Regulation Io=full load to 25% load Dual ±1.0 Line Regulation Vin=low line to high line, full load Single Dual ±0.5 Temperature Coefficient Ta=-40°C to 85°C All ±0.05 Output Voltage Ripple and Noise (5Hz to 20MHz bandwidth) Vo=3.3Vdc Vo=3.3Vdc	
Load Regulation Io=full load to 25% load Dual ±1.0 Line Regulation Vin=low line to high line, full load Single Dual ±0.5 Temperature Coefficient Ta=-40°C to 85°C All ±0.05 Output Voltage Ripple and Noise (5Hz to 20MHz bandwidth) Vo=3.3Vdc Vo=5Vdc Vo=5Vdc Vo=±5Vdc Vo=±5Vd	%/ °C
Load Regulation Io=full load to 25% load Dual ±1.0 Line Regulation Vin=low line to high line, full load Single Dual ±0.5 Temperature Coefficient Ta=-40°C to 85°C All ±0.05 Output Voltage Ripple and Noise (5Hz to 20MHz bandwidth) Vo=3.3Vdc Vo=5Vdc Vo=5Vdc Vo=±5Vdc Vin=nominal input, Io= full load Vo=±5Vdc Vo=±5	
Load Regulation Io=full load to 25% load Dual ±1.0 Line Regulation Vin=low line to high line, full load Dual ±0.5 Temperature Coefficient Ta=-40°C to 85°C All ±0.05 Output Voltage Ripple and Noise (5Hz to 20MHz bandwidth) Vo=3.3Vdc Vo=5Vdc Vo=5Vdc Vo=5Vdc Vo=±5Vdc Vin=nominal input, lo= full load Vo=±2Vdc Vo=±12Vdc Vo=±12V	%/ °C
Load Regulation Io=full load to 25% load Dual ±1.0 Line Regulation Vin=low line to high line, full load Single Dual ±0.5 Temperature Coefficient Ta=-40°C to 85°C All ±0.05 Output Voltage Ripple and Noise (5Hz to 20MHz bandwidth) Vo=3.3Vdc Vo=5Vdc Vo=5Vdc Vo=±5Vdc Vin=nominal input, lo= full load Vo=±5Vdc Vo=±5	%/ °C



Operating Output Current Range		1					
Operating Output Current Range			Vo=3.3Vdc			600	
Operating Output Current Range			Vo=5Vdc			600	
Operating Output Current Range			Vo=12Vdc			250	
Vicinity	Operating Output Current Range		Vo=15Vdc			200	mΑ
Vo=412Vdc Vo=415Vdc Vo=4							
Notes Section Notes Note							
Output DC Current-Limit Inception Vo=90% Vo_neminal Vo=30XVdc O Vo=40XVdc O Vo=4							
Maximum Output Capacitance Full load (resistive) Vos-5Vdc O O Vos-5Vdc O O Vos-5Vdc O O Vos-5Vdc O O O O O O O O O	Output DC Current Limit Incention	Va-009/ V		120		±100	0/
Maximum Output Capacitance Full load (resistive) Vos-15Vdc	Output DC Current-Limit inception	VO=90 % V _{O, nominal}				2200	70
Maximum Output Capacitance Full load (resistive) Vo=t5Vdc 0 2200 UF Vo=t5Vdc 0 1000 Vo=t5Vdc 0 Vo=t5Vdc 0 1000 Vo=t5Vdc 0 Vo=t5Vdc 0 Vo=t5Vdc 0 Vo=t5Vdc 0 Vo=t5Vdc 0 Vo=t5Vdc 0 Vo=t5Vdc							
Maximum Output Capacitance Full load (resistive) Vo=15Vdc Vo±5Vdc 0							
No=±5Vdc 0	Maximum Output Capacitance	Full load (resistive)					uF
Vo=±12Vdc 0		,					
DYNAMIC CHARACTERISTICS			Vo=±12Vdc	0			
NOTES and CONDITIONS Device Min. Typical Max. Units			Vo=±15Vdc	0		1000	
Output Voltage Current Transient Step Change in Output Current 75% to 100% of lo.max All	DYNAMIC CHARACTERIS	TICS	<u> </u>				
Output Voltage Current Transient Step Change in Output Current 75% to 100% of lo.max All ±6 % Setting Time (within 1% Yout nominal) di/dt=0.1A/us All 500 us Turn-On Delay and Rise Time Turn-On Delay Time, From Input Vin, min. to 10%Vo, set All 2 ms EFFICIENCY PARAMETER NOTES and CONDITIONS Device Min. Typical Max. Units EG3A01-E 77 EC3A02-E 80 EC3A03-E	PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Setting Time (within 1% Vout nominal) di/dt=0.1 A/us	Output Voltage Current Transient		<u> </u>				
Turn-On Delay and Rise Time Turn-On Delay Time, From Input Vin, min. to 10%Vo, set All 2 ms Output Voltage Rise Time 10%Vo, set to 90%Vo, set All 2 ms EFFICIENCY PARAMETER NOTES and CONDITIONS Device Min. Typical Max. Units EC3A01-E 77 EC3A02-E 80 EC3A03-E 80 EC3A04-E 77 EC3A05-E 80 EC3A06-E 80 EC3A06-E 80 EC3A07-E 72 EC3A11-E 81 EC3A11-E 81 EC3A12-E 84 EC3A12-E 84 EC3A13-E 85 EC3A14-E 82 EC3A15-E 84 EC3A16-E 85 EC3A17-E 78 Vin=24V EC3A2-E 86 EC3A					±6		%
Turn-On Delay Time, From Input Vin, min. to 10%Vo, set All 2 ms Output Voltage Rise Time 10%Vo, set to 90%Vo, set All 2 ms EFFICIENCY PARAMETER NOTES and CONDITIONS Device Min. Typical Max. Units EC3A01-E EC3A02-E EC3A03-E EC3A03-E EC3A04-E EC3A04-E EC3A04-E EC3A06-E EC3A06-E EC3A06-E EC3A07-E T2 INDEXISE CC3A01-E EC3A11-E EC3A11		di/dt=0.1A/us	All			500	us
Output Voltage Rise Time 10%Vo, set to 90%Vo, set	Turn-On Delay and Rise Time	1			1		
### Company of the image of the	Turn-On Delay Time, From Input	Vin, min. to 10%Vo, set	All			2	ms
NOTES and CONDITIONS Device Min. Typical Max. Units	Output Voltage Rise Time	10%Vo, set to 90%Vo, set	All			2	ms
EC3A01-E 777 EC3A02-E 80 EC3A03-E 80 EC3A04-E 777 EC3A05-E 80 EC3A06-E 80 EC3A07-E 72 EC3A11-E 81 EC3A12-E 84 EC3A13-E 85 EC3A15-E 84 EC3A15-E 84 EC3A16-E 85 EC3A17-E 78 EC3A21-E 82 EC3A22-E 86 EC3A23-E 86 Vin=24V EC3A24-E 82 EC3A26-E 85 EC3A26-E 86	EFFICIENCY						
Vin=5V	PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Vin=5V			EC3A01-E		77		
Vin=5V			EC3A02-E		80		
Vin=5V			EC3A03-E		80		
EC3A05-E 80 EC3A07-E 72 EC3A11-E 81 EC3A12-E 84 EC3A13-E 85 100% Load Vin=12V EC3A14-E 82 EC3A15-E 84 EC3A15-E 85 EC3A17-E 78 EC3A21-E 82 EC3A21-E 85 EC3A22-E 86 EC3A23-E 86 EC3A23-E 86 Vin=24V EC3A24-E 82 EC3A25-E 85 EC3A26-E 85		Vin=5V					
EC3A06-E							
EC3A07-E							
EC3A11-E 81 EC3A12-E 84 EC3A13-E 85 100% Load Vin=12V EC3A14-E 82 EC3A15-E 84 EC3A16-E 85 EC3A17-E 78 EC3A21-E 82 EC3A22-E 86 EC3A23-E 86 EC3A23-E 86 EC3A24-E 82 EC3A25-E 85 EC3A26-E 86							
EC3A12-E			+				
100% Load Vin=12V			EC3A11-E				
100% Load Vin=12V EC3A14-E EC3A15-E 84 EC3A17-E 78 EC3A21-E EC3A22-E EC3A23-E Vin=24V EC3A24-E EC3A25-E EC3A26-E 85 EC3A26-E 86			EC3A12-E		84		
EC3A15-E 84 EC3A16-E 85 EC3A17-E 78 EC3A21-E 82 EC3A22-E 86 EC3A23-E 86 Vin=24V EC3A24-E 82 EC3A25-E 85 EC3A26-E 86			EC3A13-E		85		
EC3A16-E EC3A17-E 78 EC3A21-E EC3A22-E EC3A23-E Vin=24V EC3A24-E EC3A25-E EC3A26-E EC3A26-E EC3A26-E	100% Load	Vin=12V	EC3A14-E		82		%
EC3A16-E EC3A17-E 78 EC3A21-E EC3A22-E EC3A22-E EC3A23-E Vin=24V EC3A24-E EC3A25-E EC3A26-E EC3A26-E EC3A26-E			EC3A15-E		84		
EC3A17-E 78 EC3A21-E 82 EC3A22-E 86 EC3A23-E 86 Vin=24V EC3A24-E 82 EC3A25-E 85 EC3A26-E 86					85		
EC3A21-E 82 EC3A22-E 86 EC3A23-E 86 Vin=24V EC3A24-E 82 EC3A25-E 85 EC3A26-E 86							
EC3A22-E			+				
EC3A23-E							
Vin=24V							
EC3A25-E 85 EC3A26-E 86							
EC3A26-E 86		Vin=24V					
			EC3A25-E		85		
EC3A27-E 78			EC3A26-E		86		
			EC3A27-E		78		



	EC3A31-E		84			
	EC3A32-E		86			
	EC3A33-E		86			
Vin=48V	EC3A34-E		85		%	
	EC3A35-E		87			
	EC3A36-E		87			
	EC3A37-E		79			
STICS	•				•	
NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units	
	EC3AXX (S/M/MS)-E			500		
Input to Output, 1 minutes	EC3AXX (H/HS)-E			3000	Vdc	
	EC3AXX (HM/HMS)-E			1500		
Input to Output	All	1000			ΜΩ	
Input to Output	All		250		pF	
TICS						
NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units	
	All	100			KHz	
NS						
NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units	
lo=100% of lo.max; Ta=25°C per MIL-HDBK-217F	All		2.5		M hours	
	All		12.5		grams	
	Input to Output Input to Outpu	C3A32-E EC3A33-E EC3A33-E EC3A34-E EC3A35-E EC3A36-E EC3A37-E STICS	Vin=48V EC3A32-E EC3A33-E EC3A34-E EC3A35-E EC3A36-E EC3A37-E Input to Output, 1 minutes EC3AXX (S/M/MS)-E EC3AXX (H/HS)-E EC3AXX (HM/HMS)-E EC3	EC3A32-E 86 EC3A33-E 86 Vin=48V	C3A32-E 86 EC3A33-E 86 EC3A33-E 86 EC3A34-E 85 EC3A35-E 87 EC3A37-E 79 EC3A37-E 79 EC3A37-E EC3A37-E	



Application Note V10 June 2014

5. Main Features and Functions 5.1 Operating Temperature Range

The EC3A-E series converters can be operated by a wide ambient temperature range from -40 $^{\circ}$ C to 85 $^{\circ}$ C. The standard models case temperature should not be exceeded 100 $^{\circ}$ C at normal operating (Detail see content 6.2).

5.2 UVLO (Under Voltage Lockout)

Input under voltage lockout is standard on the EC3A-E models. The unit will shut down when the input voltage drops below a threshold, and the unit will operate when the input voltage goes above the upper threshold.

5.3 Over Current Protection

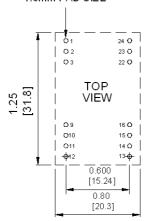
All models have internal over current and continuous short circuit protection. The unit operates normally once the fault condition is removed. At the point of current limit inception, the converter will go into over current protection.

6. Applications

6.1 Recommended Layout PCB Footprints and Soldering Information

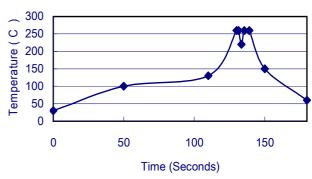
The system designer or the end user must ensure that other components and metal in the vicinity of the converter meet the spacing requirements to which the system is approved. Low resistance and low inductance PCB layout traces are the norm and should be used where possible. Due consideration must also be given to proper low impedance tracks between power module, input and output grounds. The recommended footprints and soldering profiles are shown below.

0.8mm PLATED THROUGH HOLE 1.6mm PAD SIZE



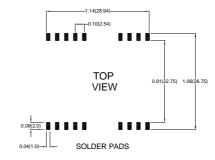
Note: Dimensions are in inches (millimeters)

Lead Free Wave Soldering Profile

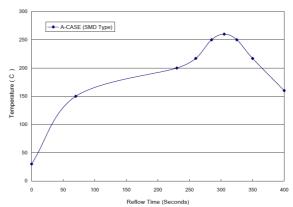


Note:

- 1. Soldering Materials: Sn/Cu/Ni
- 2. Ramp up rate during preheat: 1.4 °C/Sec (From 50°C to 100°C)
- 3. Soaking temperature: 0.5 $^{\circ}\text{C/Sec}$ (From 100 $^{\circ}\text{C}$ to 130 $^{\circ}\text{C}$), 60±20 seconds
- 4. Peak temperature: 260°C, above 250°C 3~6 Seconds
- 5. Ramp up rate during cooling: -10.0 °C/Sec (From 260°C to 150°C)



Lead Free Hot Air Reflow Profile



Note:

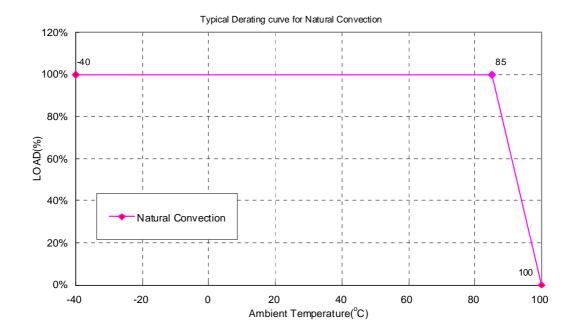
- 1. Soldering Paste: SHENMAO PF610-P (Sn/Ag/Cu)
- 2. Ramp up rate during preheat: 1.71 °C/Sec (From 30°C to 150°C)
- 4. Ramp up rate during reflow: 0.96 $^{\circ}\text{C/Sec}$ (From 217 $^{\circ}\text{C}$ to 260 $^{\circ}\text{C}$)
- 5. Peak temperature: 260°C, above 217°C 90 Seconds
- 6. Ramp up rate during cooling: -1.2 °C/Sec (From 260°C to 160°C)

Figure 3 Recommended PCB Layout Footprints and Wave Soldering Profiles for DIP-24 and SMD packages



6.2 Power De-Rating Curves for EC3A-E Series

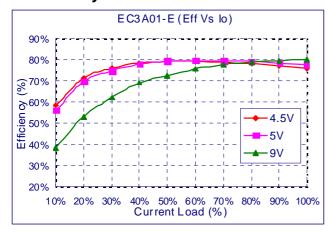
Operating Ambient temperature Range: -40°C ~ 85°C

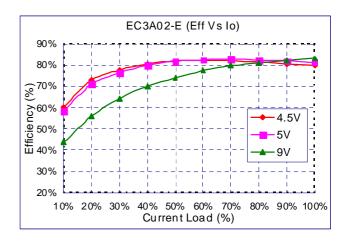


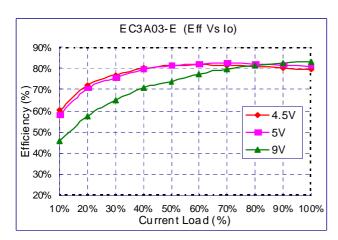


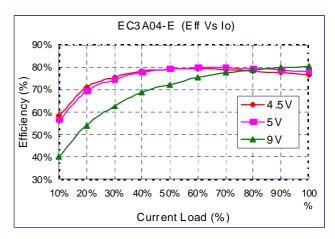
Application Note V10 June 2014

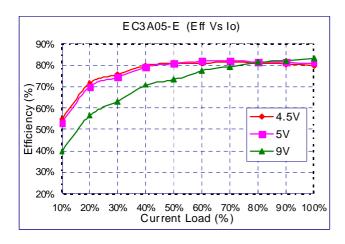
6.3 Efficiency vs. Load Curves

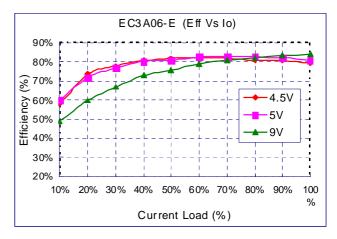




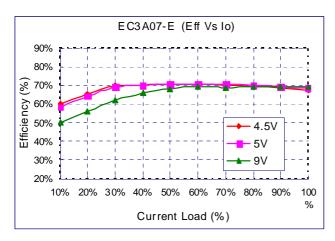


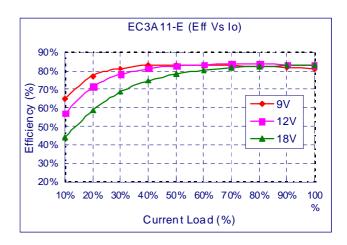


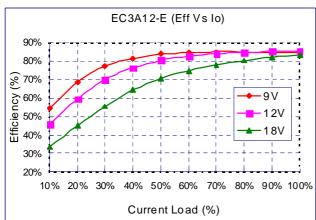


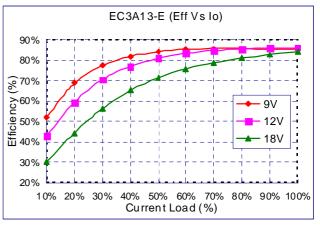


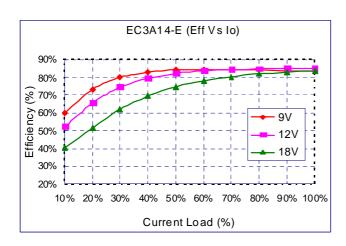


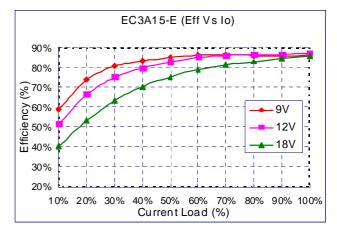




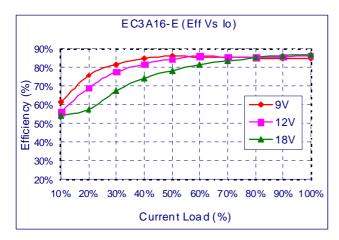


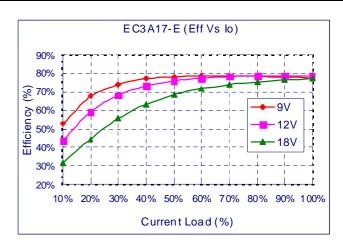


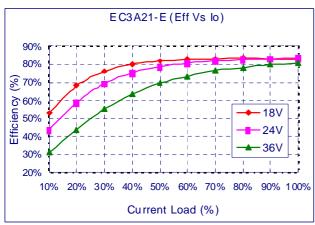


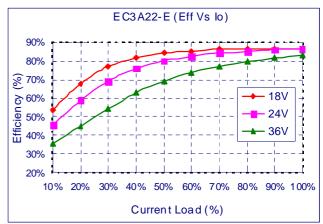


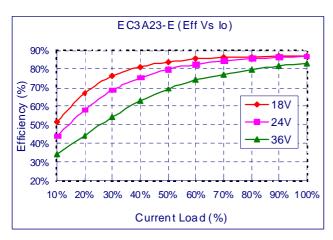


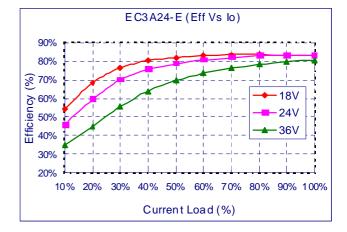




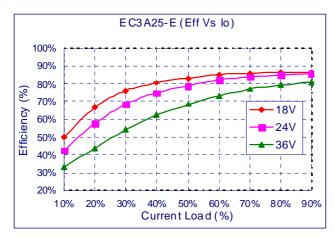


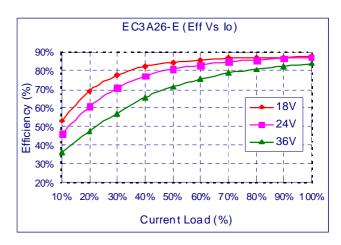


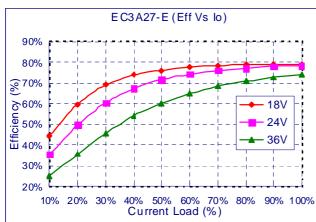


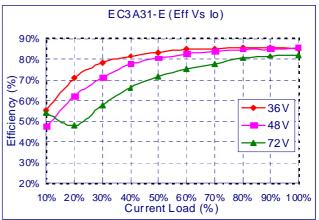


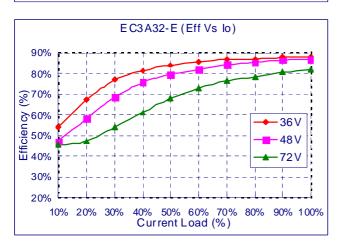


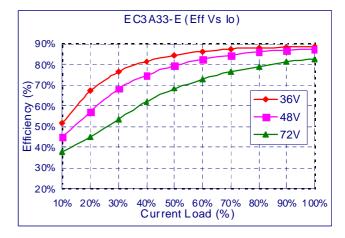




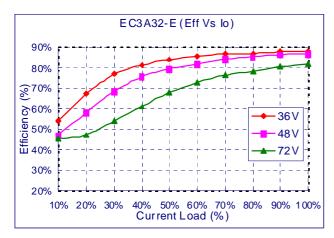


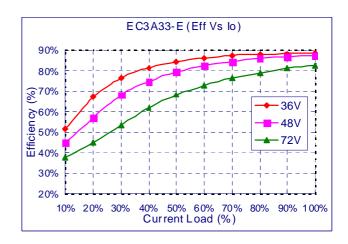


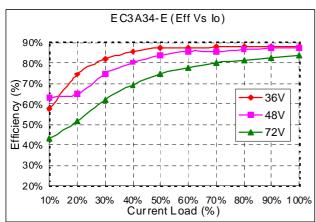


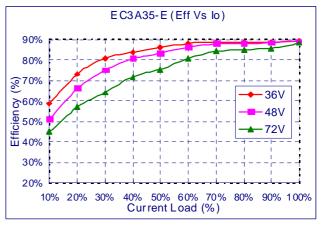


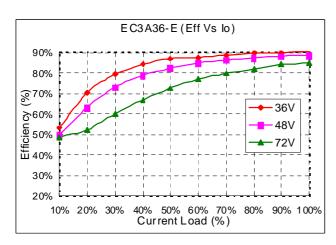


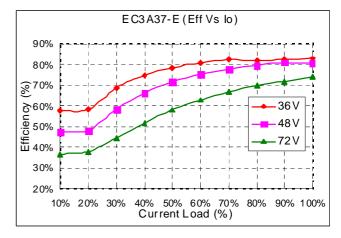










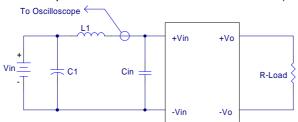




Application Note V10 June 2014

6.4 Input Capacitance at the Power Module

The converters must be connected to low AC source impedance. To avoid problems with loop stability source inductance should be low. Also, the input capacitors (Cin) should be placed close to the converter input pins to de-couple distribution inductance. However, the external input capacitors are chosen for suitable ripple handling capability. Low ESR capacitors are good choice. Circuit as shown in Figure 4 represents typical measurement methods for reflected ripple current. C1 and L1 simulate a typical DC source impedance. The input reflected-ripple current is measured by current probe tooscilloscope with a simulated source Inductance (L1).



L1: 12uH.

C1: 220uF ESR <0.1 Ω @ 20°C, 100KHz.

Cin: None

Figure 4 Input Reflected-Ripple Test Setup

6.5 Test Set-Up

The basic test set-up to measure parameters such as efficiency and load regulation is shown in Figure 5. When testing the modules under any transient conditions please ensure that the transient response of the source is sufficient to power the equipment under test. We can calculate the

- Efficiency
- · Load regulation and line regulation.

The value of efficiency is defined as:

$$\eta = \frac{Vo \times Io}{Vin \times Iin} \times 100\%$$

Where

Vo is output voltage, lo is output current, Vin is input voltage, lin is input current.

The value of load regulation is defined as:

$$Load.reg = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

Where

 V_{FL} is the output voltage at full load

 V_{NL} is the output voltage at 10% load (Single output)

V_{NL} is the output voltage at 25% load (Dual output)

The value of line regulation is defined as:

$$Line.reg = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Where

V_{HL} is the output voltage of maximum input voltage at full load.

V_{LL} is the output voltage of minimum input voltage at full load.

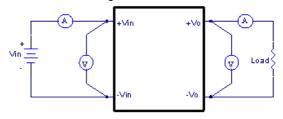
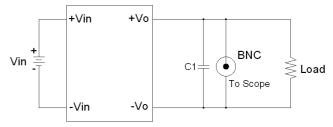


Figure 5 EC3A Series Test Setup

6.6 Output Ripple and Noise Measurement

The test set-up for noise and ripple measurements is shown in Figure 6 and 7. A coaxial cable was used to prevent impedance mismatch reflections disturbing the noise readings at higher frequencies. Measurements are taken with output appropriately loaded and all ripple/noise specifications are from 5Hz to 20MHz Band Width.



Note: C1: 0.1uF Ceramic capacitor for SMD Models Only Figure 6 Using BNC to Measure Output Ripple and Noise

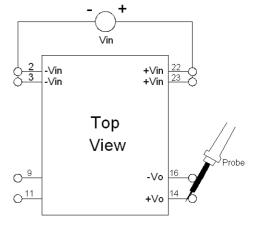


Figure 7 Using Probe to Measure Output Ripple and Noise



Application Note V10 June 2014

6.7 Output Capacitance

The EC3A-E series converters provide unconditional stability with or without external capacitors. For good transient response low ESR output capacitors should be located close to the point of load. These series converters are designed to work with load capacitance to see technical specifications.

7. Safety & EMC

7.1 Input Fusing and Safety Considerations.

The EC3A-E series converters have not an internal fuse. However, to achieve maximum safety and system protection, always use an input line fuse. We recommended a fast acting fuse 1.5A for 5Vin models, 0.8A for 12Vin models, 0.5A for 24Vin models and 0.25A for 48Vin modules. Figure 8 circuits are recommended by a Transient Voltage Suppressor diode across the input terminal to protect the unit against surge or spike voltage and input reverse voltage.

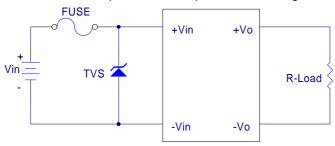


Figure 8 Input Protection

7.2 EMC Considerations

EMI Test standard: EN55022

Test Condition: Input Voltage: Nominal, Output Load: Full Load

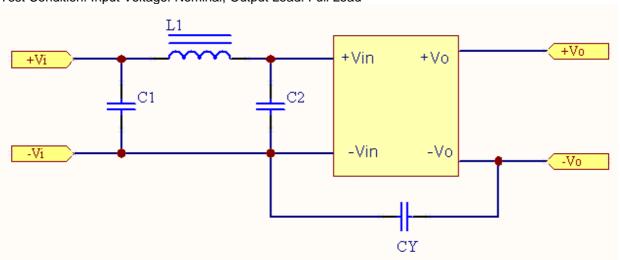


Figure 9 Connection circuit for conducted EMI testing

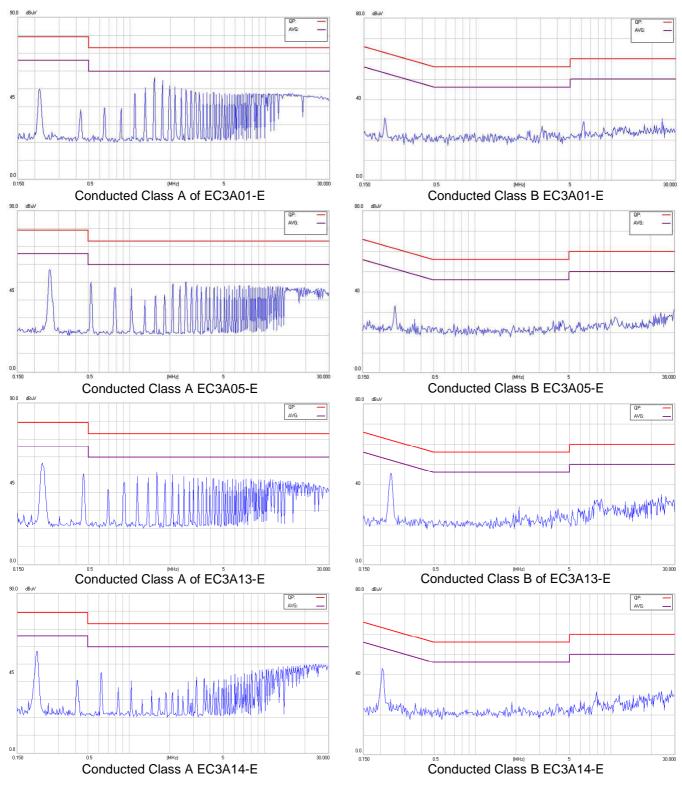


Application Note V10 June 2014

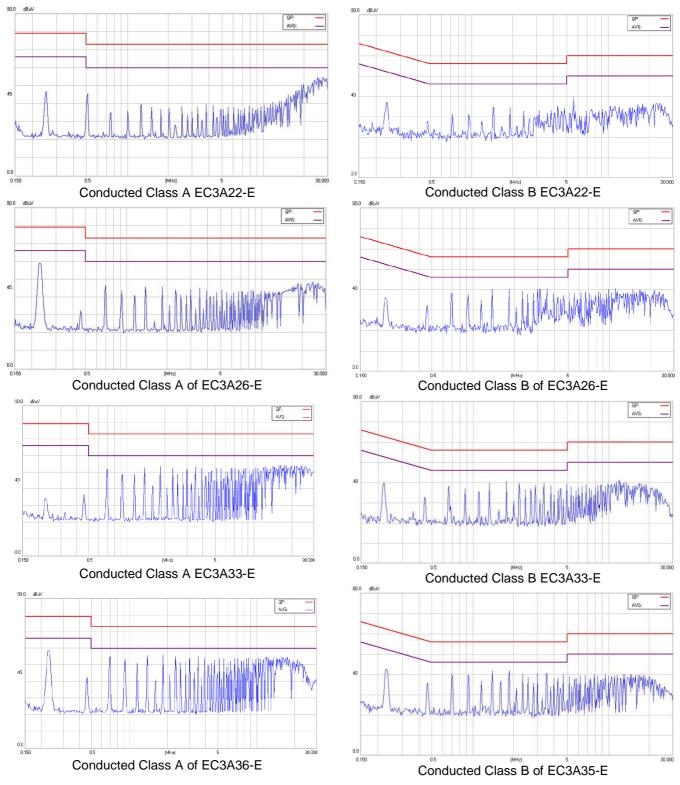
MadalNa	EN55022 Class A EN55022 C						ass B	
Model No.	C1	C2	L1	CY	C1	C2	L1	CY
EC3A01-E	NC	NC	NC	NC	3.3uF/50V	NC	3.3uH	NC
EC3A02-E	NC	NC	NC	NC	3.3uF/50V	NC	3.3uH	NC
EC3A03-E	NC	NC	NC	NC	3.3uF/50V	NC	3.3uH	NC
EC3A04-E	NC	NC	NC	NC	3.3uF/50V	NC	3.3uH	NC
EC3A05-E	NC	NC	NC	NC	3.3uF/50V	NC	3.3uH	NC
EC3A06-E	NC	NC	NC	NC	3.3uF/50V	NC	3.3uH	NC
EC3A07-E	NC	NC	NC	NC	3.3uF/50V	NC	3.3uH	NC
EC3A11-E	NC	NC	NC	NC	1.0uF/50V	NC	5.6uH	NC
EC3A12-E	NC	NC	NC	NC	1.0uF/50V	NC	5.6uH	NC
EC3A13-E	NC	NC	NC	NC	1.0uF/50V	NC	5.6uH	NC
EC3A14-E	NC	NC	NC	NC	1.0uF/50V	NC	5.6uH	NC
EC3A15-E	NC	NC	NC	NC	1.0uF/50V	NC	5.6uH	NC
EC3A16-E	NC	NC	NC	NC	1.0uF/50V	NC	5.6uH	NC
EC3A17-E	NC	NC	NC	NC	1.0uF/50V	NC	5.6uH	NC
EC3A21-E	NC	NC	NC	NC	3.3uF/50V	NC	5.6uH	470pF/3KV
EC3A22-E	NC	NC	NC	NC	3.3uF/50V	NC	5.6uH	470pF/3KV
EC3A23-E	NC	NC	NC	NC	3.3uF/50V	NC	5.6uH	470pF/3KV
EC3A24-E	NC	NC	NC	NC	3.3uF/50V	NC	5.6uH	470pF/3KV
EC3A25-E	NC	NC	NC	NC	3.3uF/50V	NC	5.6uH	470pF/3KV
EC3A26-E	NC	NC	NC	NC	3.3uF/50V	NC	5.6uH	470pF/3KV
EC3A27-E	NC	NC	NC	NC	3.3uF/50V	NC	5.6uH	470pF/3KV
EC3A31-E	NC	NC	NC	NC	2.2uF/100V	NC	10uH	470pF/3KV
EC3A32-E	NC	NC	NC	NC	2.2uF/100V	NC	10uH	470pF/3KV
EC3A33-E	NC	NC	NC	NC	2.2uF/100V	NC	10uH	470pF/3KV
EC3A34-E	NC	NC	NC	NC	2.2uF/100V	NC	10uH	470pF/3KV
EC3A35-E	NC	NC	NC	NC	2.2uF/100V	NC	10uH	470pF/3KV
EC3A36-E	NC	NC	NC	NC	2.2uF/100V	NC	10uH	470pF/3KV
EC3A37-E	NC	NC	NC	NC	2.2uF/100V	NC	10uH	470pF/3KV

Note: All of capacitors are ceramic capacitors.





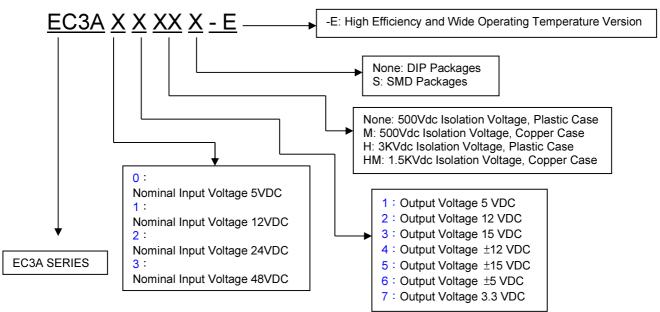




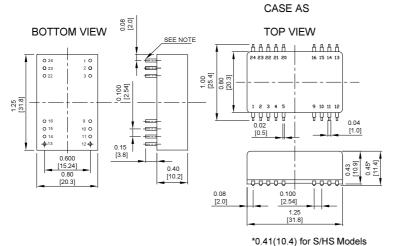


Application Note V10 June 2014

8. Part Number



9. Mechanical Specifications



	PIN CONNECTION											
		500 VE	C		1.5K & 3K VDC							
Pin	Single	Output	Dual Output		Pin	Single	Output	Dual	Output			
	DIP	SMD	DIP	SMD		DIP	SMD	DIP	SMD			
1,24	+V Input		+V I	nput	1,24	NP	NC	NP	NC			
2,23	N	NC		-V Output		-V I	nput	-V I	nput			
3,22	N	С	Cor	nmon	4,5	NP NC		NP	NC			
4	NP	NC	NP	NC	9	NC		Common				
5	NP	NC	NP	NC	10,15	10,15 NC		NC				
9	NP	NC	NP	NC	11	N	NC -V Out		Output			
10,15	-V C	Output	Cor	nmon	12,13	NP	NC	NP	NC			
11,14	+V (Output	+V (+V Output		+V Output		+V Output				
12,13	-V Ir	nput	-V Input		16	-V (Output	Common				
16	NP	NC	NP	NC	20,21	20,21 NP NC		NP	NC			
20,21	NP	NC	NP	NC	22,23 +V Input		put +V Input					

^{*} NP-NO PIN * NC-NO CONNECTION WITH PIN

NOTE:Pin Size is 0.02 ±0.002 Inch (0.5±0.05 mm)DIA All Dimensions In Inches (mm) Tolerances Inches: X.XX= ±0.02 . X.XXX= ±0.010

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